Notice of References Cited Application/Control No. 09/889,558 Examiner Jennifer M. Dolan Applicant(s)/Patent Under Reexamination LEROUX, CHARLES Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5,736,779	04-1998	Kobayashi, Kenya	257/603
	В	US-5,536,958	07-1996	Shen et al.	257/356
	O	US-5,365,099	11-1994	Phipps et al.	257/328
	۵	US-5,610,790	03-1997	Staab et al.	361/56
	ш	US-			
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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	0					
	Ф					
	σ					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

* Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Cohen, Seymour H. et al., "An Improved Input Protection Circuit for C-MOS/SOS Arrays; August 1978. IEEE Transactions of Electron Devices, Vol 25, No 8. pp 926-932.	
	٧		
	w		
	x		

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.